Master Thesis/ Intern (m/f) - Interference Channel Analysis
Job No. 3090067, Garching

About us
GE Aviation Munich is a R&D center of excellence and is in the heart of southern Germany, on the Garching campus of the Technical University of Munich. This creates a unique blend for our engineers to be in a university setting, while performing research and development in a world-class industrial environment that is dedicated to bringing innovative technologies to market. Within the R&D community, the center maintains close partnerships with numerous universities, research institutions and technology companies in Germany and abroad.

Responsibilities
• Investigate domain specific literature (CAST-32A) which will give you a guideline and direction
• Identify interference channels by using the specifications of both architectures mentioned above
• Perform a state-of-the-art search of existing test suites that help to exercise and identify interference channels
• Characterize each identified test suite's interference channel's analysis capability and granularity of results
• Implement a test suite based on the existing ones and successfully run it on both architectures in the lab

Qualifications
• Student of electrical engineering or computer science
• Good C/C++ Skills
• Good understanding of MPSoCs
• Good understanding of real-time operating systems (e.g. RTLinux, FreeRTOS, WindRiver VXWorks)
• Fluency in German and English

If you have any further requests, please contact Anna Lena Mehnert, Recruiter, at Tel. +49 1721670751.
We look forward to receiving your online application!

For more information and current job openings visit ge.com/careers