

Module:	Software Reliability: Runtime Verification and Runtime Reflection
Lecturer:	MSc Malte Schmitz
Language:	English
Teaching Method:	Lecture and practical exercise
Credit Points:	1 ECTS
Attendance requirements:	Basics in computer science and mathematics, especially logic, to the extent which is characteristic for a third term student in computer science
Goals / Skill:	<p>The student will learn about techniques for monitoring systems at runtime and to build automatic means for healing systems in case of unforeseen behaviour. He or she will study monitor synthesis approaches as well as basics of controller synthesis to reconfigure systems at runtime.</p> <p>The focus of the module is on understanding the concepts and mechanisms of runtime verification and runtime reflection.</p> <p>The goal is to enable the student to apply runtime verification techniques to enrich the reliability of systems.</p>
Detailed Content:	<ul style="list-style-type: none"> • Specification of correctness properties. • Monitor synthesis algorithms. • Diagnosis and Reconfiguration. • Code manipulation. • jUnitRV
Media Used:	Electronic Presentation, Blackboard Illustrations, Practical Demonstrations
Literature:	<ul style="list-style-type: none"> • Martin Leucker: Teaching Runtime Verification, RV 2011, Springer, 2012
Tutorial:	The student will apply the runtime verification techniques learned during the course in practical applications. The tutorial involves the usage of monitor synthesis and Java or C code instrumentation tools.
Suggested Reading before the start of the summer school:	<ul style="list-style-type: none"> • A text on model checking could be helpful. For example: Baier and Katoen: Principles of Model Checking, The MIT Press, 2010.